Electronic	Acknowledgement Receipt			
EFS ID:	5331275			
Application Number:	10616751			
International Application Number:				
Confirmation Number:	2335			
Title of Invention:	Stacked gate flash memory cell with reduced disturb conditions			
First Named Inventor/Applicant Name:	Peter W. Lee			
Correspondence Address:	George O. Saile - 28 Davis Avenue - Poughkeepsie NY 12603 US			
Filer:	William Breen/Kenneth Linder			
Filer Authorized By:	William Breen			
Attorney Docket Number:	AP99-002B			
Receipt Date:	13-MAY-2009			
Filing Date:	10-JUL-2003			
_	19:09:18			
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1		A DED 0007DO A 16	98708	yes	3	
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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.